

(19)



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(11)

EP 0 734 078 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:
25.09.1996 Bulletin 1996/39

(51) Int. Cl.⁶: H01L 33/00, H01L 27/15,
H01L 51/00

(21) Application number: 96104504.4

(22) Date of filing: 21.03.1996

(84) Designated Contracting States:
DE FR GB

(30) Priority: 22.03.1995 US 409129

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(54) Full color organic light emitting diode array

(57) An organic full color light emitting diode array including a plurality of spaced apart, light transmissive electrodes (101) formed on a substrate (100), a plurality of cavities (10, 20) defined on top of the electrodes (101) and three electroluminescent media (202, 203, 204) designed to emit three different hues deposited in the cavities (10, 20). A plurality of spaced metallic electrodes (106, 108, 110, 115) arranged orthogonal to the transmissive electrodes (101) and formed to seal each of the cavities (10, 20), thereby, sealing the electroluminescent media (202, 203, 204) in the cavities (10, 20), with a light transmissive anodic electrode (101) at the bottom of each cavity (10, 20) and an ambient stable cathodic metallic electrode (106, 108, 110, 115) on the top of each cavity (10, 20). Methods of fabrication of a full color organic light emitting diode array are also disclosed.

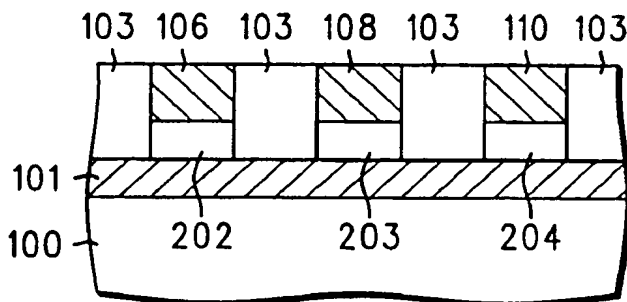


FIG. 5

EP 0 734 078 A2

Description

Field of the Invention

The present invention relates to organic light emitting diode image manifestation apparatus and to a novel method of fabrication of a full color organic light emitting diode (LED) array for image manifestation apparatus.

Background of the Invention

An organic LED array for image display applications is composed of a plurality of organic light emitting pixels arranged in rows and columns. To generate a full color display from a thin film electroluminescent array, there are two primary technologies known in the prior art. A full color array can be achieved by constructing three sub-pixels in one pixel, each sub-pixel emitting red, green or blue. This technology is generally utilized in cathode-ray-tube color displays. An alternative full color array makes use of a white emitter as a backlight in conjunction with a color filter array containing pixels patterned into red, green, and blue sub-pixels. The second technology to generate full color has been widely used in full color liquid crystal displays.

In a full color organic electroluminescent display, the color filter based technology is generally considered less favorable due to the luminescent efficiency limits of most organic LED devices.

In a full color array, each individual organic light emitting pixel is divided into red, green and blue sub-pixels. Each sub-pixel is generally constructed with a light transmissive first electrode, an organic electroluminescent medium deposited on the first electrode, and a metallic electrode on top of the organic electroluminescent medium. The color of the sub-pixel is determined by the electroluminescent medium employed. The electrodes connect the pixels to form a two-dimensional X-Y addressing pattern. In practice, the X-Y addressing pattern is achieved by patterning the light transmissive electrodes in the X direction and patterning the metallic electrodes in the Y direction, with X and Y directions being perpendicular to each other. The patterning of the electrodes is usually accomplished by either shadow mask or etching techniques. Due to the technical limits of shadow masks, only etching processes are being utilized for image displays, which have generally sub-pixel pitches less than 0.2 mm.

Depending on the medium used in etching processes, the etching technique can be divided into two categories: wet and dry. While wet etching is performed in an acidic liquid medium, dry etching is usually carried out in a plasma atmosphere.

Scozzafava in EP 349,265 disclosed an organic electroluminescent image display device and a process for its fabrication based on a wet etching technology. The metallic electrodes used for cathode contacts in organic LEDs usually contain a stable metal and a

highly reactive metal (with work function less than 4 eV) that can not survive in the acid etching processes.

The dry etching processes for metallic electrodes in an organic LED array is also problematic. The high temperature ($> 200^{\circ}\text{C}$) and reactive ion atmosphere required in dry etching process may affect the integrity of the organic materials as well as the active metal contained in the metallic electrodes in an organic LED array.

Tang in U.S. Patent Number 5,276,380 discloses a shadow wall method to fabricate the two-dimensional array without the need of the etching of the metallic electrodes. The shadow wall method includes: patterning the transparent electrode first; building dielectric walls that are orthogonal to the transparent electrodes, capable of shadowing an adjacent pixel area, and with a height exceeding the thickness of the organic medium; depositing organic electroluminescent medium; and depositing the cathode metals from an angle of 15° to 45° with respect to the deposition surface. Since the height of the dielectric walls exceeds the thickness of the organic medium, isolated parallel metal stripes are formed. Thus, a X-Y addressable array is achieved without the need of metal etching. Though this method seems to be viable for metal patterning, it is limited to certain pitch dimensions, and potentially could introduce leakage in pixels in the array.

Thus, it would be advantageous to provide a full color organic LED array which overcomes these problems.

Accordingly, it is a purpose of this invention to provide a novel method of fabrication of a full color organic LED array for image display applications.

It is another purpose of this invention to provide a cavity structure in which a full color organic LED array is constructed.

It is a further purpose of this invention to provide a passivated full color organic LED array for image display applications with improved reliability.

Summary of the Invention

The above problems and others are at least partially solved and the above purposes and others are realized in a full color organic light emitting diode array including an electrically insulative, light transmissive substrate having a planar surface. A plurality of laterally spaced, light transmissive, electrically conductive strips are arranged in rows on the surface of the substrate with portions of the surface of the substrate exposed therebetween and a layer of dielectric medium is deposited over the conductive strips and the portions of the surface of the substrate. The dielectric medium is etched to define a plurality of cavity structures, with each cavity structure defining a sub-pixel, and the plurality of cavity structures being positioned in rows in overlying relationship to the plurality of conductive strips with individual cavity structures positioned in overlying relationship to an associated conductive strip so as to expose a portion

of the conductive strip. Three electroluminescent media, each including at least a layer of active emitter material and a layer of a low work function metal are laterally and alternatively deposited in the cavity structures on the exposed associated conductive strip, with all of the cavity structures in each individual row containing only one kind of the three electroluminescent media. An ambient stable metal is positioned in sealing engagement over the cavity structures and connected to form a plurality of laterally spaced, parallel, metallic strips arranged in columns orthogonal to the conductive strips to complete the array.

Methods of fabrication of an full color organic light emitting diode array are also disclosed wherein some of the above steps are repeated to form two or three groups of sub-pixels.

Brief Description of the Drawing

Referring to the drawings, wherein like characters indicate like parts throughout the figures:

FIG. 1 is a plan view of different cavity structures intentionally depicted on the same substrate to illustrate their dimensional difference;

FIGS. 2, 3, 4 and 5 are cross sectional views at successive stages of fabrication of a pixel with three sub-pixels of an organic LED array embodying the present invention; and

FIG. 6 is a top plan view of the organic LED array with portions broken away for ease of visualization.

Description of the Preferred Embodiments

Since device feature dimensions are often in sub-micrometer ranges, the drawings are scaled for ease of visualization rather than dimensional accuracy. Referring specifically to FIG. 1, a plan view of trenches 10 and wells 20 are intentionally depicted on the same substrate to illustrate their dimensional difference. Both the trench and the well structures are generally formed by photolithographically patterning a dielectric layer 103 that has been deposited on top of light transmissive, conductive strips that are in turn supported by an underlying layer of transparent insulating substrate, as will be described presently.

Each trench 10 is defined as a long, narrow, straight, deep depression with four relative steep (substantially vertical) sides formed in dielectric layer 103. Each trench 10 typically takes the shape of a rectangular parallelepiped as shown in FIG. 1. A trench 10 generally extends across substrate 100 in a direction either perpendicular to underlying light transmissive, conductive strips 101 (see FIG. 6 for example) or parallel to and on the top of underlying light transmissive, conductive strips 101. In this specific embodiment, trenches 10 are preferably perpendicular to underlying light transmissive, conductive strips 101 so as to form a x-y address-

able matrix. A plurality of pixels can be constructed in each single trench 10.

Each well 20 is defined as a hole formed in dielectric layer 103 with a rectangular, square or circular shape of opening, and steep (substantially vertical) side walls. Wells 20 are characterized by the small feature size and nearly isotropic shape of opening. A plurality of wells 20 are constructed in a row across substrate 100 in overlying relationship to light transmissive, conductive strips 101 (see FIG. 6 for example) Each well 20 defines the shape of a sub-pixel in the array. Either trenches 10 or wells 20 can be used in fabrication of a full color organic LED array for image display, as will be described presently. For convenience of further disclosure, both trenches 10 and wells 20 are hereinafter referred to in general as cavity structures, which term also includes other variations on these structures.

The LED array to be described is characterized in that the array is capable of full color image display. Each pixel is composed of three sub-pixels, each having a specific electroluminescent medium capable of emitting light of a different hue, preferably red, or green, or blue. Each sub-pixel is constructed with a cavity structure having light transmissive, conductive strips 101 at the bottom, dielectric medium on the side walls and an ambient stable metal on the top. A method of fabrication of the full color organic LED array for image display applications, in accordance with the present invention, is disclosed herein. FIGS. 2, 3, 4 and 5 are cross sectional views illustrating a single pixel with three sub-pixels of the LED array at successive stages of fabrication.

Referring to FIG. 2, the construction of a pixel begins with a relative flat, light transmissive, preferably transparent, and electrically insulative substrate 100. Substrates made of glass and/or polymeric materials can generally be used, but glass is preferred. A layer of light transmissive, electrically conductive material is deposited on the upper surface of substrate 100, which layer is selected from a variety of organic or inorganic conductors, such as conductive polyaniline (PANI), or indium-tin-oxide (ITO). The layer is then patterned by conventional lithographic techniques to form a plurality of parallel conductive strips 101 that are capable of being addressed in a row fashion (or, conversely, a column fashion if desired) and will serve as an anodic electrode in the final array.

A layer of dielectric medium 103 is deposited on top of conductive strips 101 and exposed portions of substrate 100, by techniques such as thermal evaporation, sputtering or plasma enhanced chemical vapor deposition. Dielectric medium 103 used in the construction of cavity structures can be an organic polymer or inorganic materials. It is preferred to use an inorganic dielectric material such as silicon dioxide, silicon nitride, or alumina, which is usually a better barrier to oxygen and moisture than organic polymer materials. The thickness of dielectric medium 103 determines the depth of the cavity structures and may vary from 10 μm to 0.1 μm .

For easy processing, a thickness of less than 1 μm is preferred.

A layer 105 of photoresist is spin-coated on top of dielectric medium 103 and patterned with a light source through a mask (not shown). Though both positive and negative resist can be used, it is preferred to use a positive resist for its high resolution and excellent dry etching resistance. After a heat treatment, layer 105 of photoresist is developed to expose underlying dielectric medium 103. Dielectric medium 103 is then patterned by conventional wet or dry etch technique to form a cavity structure or structures 112. Dry etch is generally preferred for its anisotropic character that renders relatively straight or substantially vertical side walls.

An electroluminescent medium 202 is deposited inside cavity structure 112, and in overlying relationship to a single conductive strip of the plurality of conductive strips 101. Electroluminescent medium 202 generally consists of a layer of hole transporting material, a layer of active emitter material capable of emitting the first hue, a layer of electron transporting material and a layer of a low work functional metal as a cathode. Since these individual layers of electroluminescent material 202 are known, each individual layer is not described in detail herein.

The upper end, or opening, of cavity structure 112 is then sealed by deposition of a thick layer 106 of an ambient stable metal such as aluminum, silver, copper or gold as a cap for cavity structure 112. The thickness of layer 106 is controlled so that cavity structure 112 is just filled to the height of dielectric medium 103. A lift-off of photoresist layer 105 is performed to complete the first sub-pixel capable of emitting a first hue, red in this specific embodiment, in the pixel.

Referring to FIG. 3, the formation of a second sub-pixel is depicted. A layer 107 of photoresist is spin-coated on top of the first sub-pixel and dielectric medium 103 and patterned and developed, generally as described above. The exposed dielectric medium 103 is then patterned to form a second cavity structure or structures 113 adjacent to the first sub-pixel. An electroluminescent medium 203 is deposited in cavity structure 113 and in overlying relationship to the single conductive strip of the plurality of conductive strips 101. As explained in conjunction with electroluminescent medium 202, electroluminescent medium 203 generally consists of a layer of hole transporting material, a layer of active emitter material capable of emitting a second hue, a layer of electron transporting material and a layer of a low work function metal as a cathode. The upper end of cavity structure 113 is sealed by deposition of a thick layer 108 of an ambient stable metal such as aluminum, silver, copper or gold to the height of dielectric medium 103. After lift-off is performed on photoresist 107, the construction of the second sub-pixel in the pixel, capable of emitting the second hue, in this specific embodiment green, is complete.

Referring to FIG. 4, the formation of a third sub-pixel is depicted. A layer 109 of photoresist is spin-

coated on top of the first and second sub-pixels and dielectric medium 103 and patterned and developed, generally as described above. The exposed dielectric medium 103 is then patterned to form a third cavity structure or structures 114 adjacent to the first and second sub-pixels. An electroluminescent medium 204 is deposited in cavity structure 114 and in overlying relationship to the single conductive strip of the plurality of conductive strips 101. As explained in conjunction with electroluminescent medium 202 and 203, electroluminescent medium 204 generally consists of a layer of hole transporting material, a layer of active emitter material capable of emitting a third hue, a layer of electron transporting material and a layer of a low work function metal as a cathode. The upper end of cavity structure 114 is sealed by deposition of a thick layer 110 of an ambient stable metal such as aluminum, silver, copper or gold to the height of dielectric medium 103. After lift-off is performed on photoresist 109, the construction of the third sub-pixel in the pixel, capable of emitting the third hue, in this specific embodiment blue, is complete.

The complete pixel capable of emitting three primary colors is illustrated in FIG. 5. After the lift-off of the last photoresist layer 109, the sub-pixels are then connected in a direction orthogonal to conductive strips 101 by metal strips 115. The metal strips can be formed by either lithographically patterning a blanket layer of deposited metal or depositing of metal over a resist-masked pattern followed by a lift-off process.

FIG. 6 is a plan view of the completed organic LED array with portions broken away to illustrate three different stages of the fabrication. Proceeding from left to right in FIG. 6, an area 121 illustrates the LED array at a stage where patterned light transmissive, conductive strips 101 have been formed on light transmissive, electrically insulative substrate 100. A central area 122 in FIG. 6 illustrates the stage where individual sub-pixels of electroluminescent media 202, 203, and 204 capable of emitting three hues, in this embodiment the primary colors red, green and blue, have been deposited in cavity structures 112, 113 and 114, respectively. An area 123 illustrates the array after a plurality of parallel, laterally spaced, ambient stable metal strips 115 have been selectively deposited on top of the cavity structures 112, 113 and 114, to connect the sub-pixels in columns as cathodic electrodes.

While the above cavity structures 112, 113 and 114 have been depicted as wells, it should be understood that an array of trench structures can be fabricated in similar fashion, with the exception that the orientation of the trench structures is, preferably, perpendicular to and across all conductive strips 101 in order to form a x-y addressable matrix.

The number of pixels and the sub-pixel pitch, that is the width of a cavity structure, in an array, needed for an image display application, are dependent upon the resolution and size of the display required for the specific application. For example, a minimum of 640 x 480 pix-

els, each having three sub-pixels and with a sub-pixel pitch less than 0.1 mm, are needed for satisfactory resolution in a 13 inch diagonal color VGA type of display. The pixel pitch is confined only by the limit of lithography technology, which is around 0.5 μm in current manufacturing technology.

The array disclosed in this embodiment has inherently superior stability over any of the arrays disclosed in the prior art. The organic electroluminescent medium and the cathode contact of low work function metal in each sub-pixel (well structure) or row of sub-pixels (trench structure) are enclosed in a well or trench by a light transmissive first electrode at the bottom, a dielectric medium on the side and an ambient stable metal cap on the top. The disclosed cavity structures significantly reduce the ambient (oxygen and moisture) degradation of the display elements.

The materials used as the electroluminescent medium in the array disclosed in this invention can take any of the forms of organic EL devices disclosed in the prior art. The electroluminescent medium generally consists of a layer of hole transporting material, a layer of active emitter, a layer of electron transporting material and a layer of low work functional metal as cathode. Organic, organometallic, polymeric or any combination of those materials can be used as hole transporting materials, active emitters and electron transporting materials. It will of course be understood that in some special applications either or both of the hole transporting material and the electron transporting material may be eliminated, although this will generally result in poor light emission.

To achieve the three primary color emissions, namely, red, green and blue, three different active emitters that would generate the desired color emissions in a pixel can be utilized. In each active emitter layer, a fluorescent dopant used for enhancement of the emission efficiency for each individual color in a sub-pixel can also be incorporated. Alternatively, a single emitter capable of blue emission is used as host emitters in all three sub-pixels. To achieve the three primary color emissions, three sub-pixels are then doped with three guest dopants of efficient fluorescent dyes capable of red, green and blue emissions, respectively. The guest dopants act as the real emitters while the host emitter serves as an excited energy transporting medium.

The organic electroluminescent media are deposited by vacuum evaporation when sublimable materials are used. They can also be deposited by other techniques such as injection-fill, spin-coating, roll-coating, dip-coating or doctor-blading from a suitable solution when a polymeric material is used. A mixture of the above-mentioned techniques may be needed in cases where a heterostructure array composed of both sublimable materials and polymers is to be built.

At present time, metals with a work function less than 4.0 eV, e.g. lithium, magnesium, indium, calcium etc., are generally used as the cathode material in order to achieve an efficient LED display. It is conceivable in

the near future, metals with work function greater than 4.0 eV may also be employed as the cathode material to yield an efficient LED display as the electron injecting and transporting abilities of the materials used in the electroluminescent media improve.

In operation a pattern of light emission from the LED array can be seen from the bottom surface of transparent substrate 100. The LED array is driven to emit light by a programmed electronic driver, which sequentially addresses one row of sub-pixels at a time and repeats the addressing sequence at such a rate that the interval between repeated addressing of the same row is less than the detection limit of the human eye, typically less than 1/60th of a second. The eye sees an image formed by the light emission from all of the addressed rows, though the device at any moment is emitting from only one row.

Colors for an image are generated by addressing a combination of sub-pixels in each pixel, generally as follows:

- (1) address one sub-pixel to emit red light;
- (2) address one sub-pixel to emit green light;
- (3) address one sub-pixel to emit blue light;
- (4) simultaneously address two sub-pixels that emit red and green to generate a perception of yellow;
- (5) simultaneously address two sub-pixels that emit red and blue to generate a perception of purple;
- (6) simultaneously address two sub-pixels that emit blue and green to generate a perception of blue-green;
- (7) simultaneously address all sub-pixels to generate white light; and
- (8) address none of the sub-pixels to generate a black background.

It should be emphasized that the fabrication methods disclosed in this invention for an organic LED array capable of full color are also applicable to the fabrication of an array for either a monochrome or a multicolor (other than full color) image. With the selection of either monochrome or multicolor electroluminescent media in a pixel, an array for a monochrome or a multicolor image display application can be fabricated by the same methods disclosed in the foregoing.

Thus, a full color organic LED array for image display and its fabrication methods are disclosed. While we have shown and described specific embodiments of the present invention, further modifications and improvements will occur to those skilled in the art. We desire it to be understood, therefore, that this invention is not limited to the particular forms shown and we intend in the appended claims to cover all modifications that do not depart from the spirit and scope of this invention.

Claims

1. A full color organic light emitting diode array characterized by:

an electrically insulative, light transmissive substrate (100) having a planar surface;

a plurality of laterally spaced, light transmissive, electrically conductive strips (101) arranged in rows on the surface of the substrate (100) with portions of the surface of the substrate (100) exposed therebetween;

a layer of dielectric medium (103) deposited over the conductive strips (101) and the portions of the surface of the substrate (100);

a plurality of cavity structures (10, 20) defined by the dielectric medium (103) and each cavity structure (10, 20) defining a sub-pixel, the plurality of cavity structures (10, 20) being positioned in rows in overlying relationship to the plurality of conductive strips (101) with individual cavity structures (10, 20) positioned in overlying relationship to an associated conductive strip (101) so as to expose a portion of the conductive strip (101);

three electroluminescent media (202, 203, 204), each including at least a layer of active emitter material and a layer of a low work function metal laterally, alternatively deposited in the cavity structures (10, 20) on the exposed associated conductive strip (101), with all cavity structures (101) in each individual row containing only one kind of the three electroluminescent media (202, 203, 204); and an ambient stable metal (106, 108, 110) positioned in sealing engagement over the cavity structures (10, 20) and connected to form a plurality of laterally spaced, parallel, metallic strips (115) arranged in columns orthogonal to the conductive strips (101).

2. A full color organic light emitting diode array as claimed in claim 1 further characterized in that the light transmissive, electrically conductive strips (101) include one of an organic polymer or an inorganic material, preferably, indium-tin-oxide.

3. A full color organic light emitting diode array as claimed in claim 1 further characterized in that the dielectric medium (103) defining the cavity structures (10, 20) includes one of an organic polymer or an inorganic material.

4. A full color organic light emitting diode array as claimed in claim 3 further characterized in that the dielectric medium (103) defining the cavity structures (10, 20) includes an inorganic material including one of silicon dioxide, silicon nitride, or alumina.

5. A full color organic light emitting diode array as claimed in claim 1 further characterized in that the thickness of the dielectric medium (103) is in a range of 10 μm to 0.1 μm .

6. A full color organic light emitting diode array as claimed in claim 1 further characterized in that the three electroluminescent media (202, 203, 204) further include a layer of hole transporting material and a layer of electron transporting material positioned to sandwich the layer of active emitter material therebetween.

7. A full color organic light emitting diode array as claimed in claim 6 further characterized in that the layers of hole transporting material, active emitter material and electron transporting material include forms of organic, organometallic, polymeric or combinations of organic, organometallic, and polymeric material.

8. A full color organic light emitting diode array as claimed in claim 1 further characterized in that the three electroluminescent media (202, 203, 204) are selected to provide, in cooperation, full color emission by including three different active emitter materials that generate three primary colors, each in a different sub-pixel of a pixel.

9. A full color organic light emitting diode array as claimed in claim 1 further characterized in that the active emitter material of the three electroluminescent media (202, 203, 204) include a single blue host emitter material which is doped in each different one of the three electroluminescent media (202, 203, 204) with a different one of three guest dopants of efficient fluorescent dyes selected for three different hue emissions.

10. A method of fabricating a color organic light emitting diode array characterized by the steps of:

providing a relatively flat, light transmissive and electrically insulative substrate (100) having a planar surface;

depositing a layer of light transmissive, electrically conductive material (101) on the upper surface of the substrate (100);

patterning the layer of light transmissive, electrically conductive material to form a plurality of laterally spaced, conductive strips (101) with portions of the substrate (100) exposed therebetween;

depositing a layer of dielectric medium (103) on top of the conductive strips (101) and the portions of the substrate (100);

spin-coating a layer of photoresist on top of the dielectric medium (103);

patterning the layer of photoresist through a well structure mask to expose portions of the dielectric medium (103);

etching the exposed portions of the dielectric medium (103) to define laterally spaced cavity structures (10, 20) in overlying relationship to

the conductive strips (101), each cavity structure (10, 20) exposing a portion of an associated conductive strip (101);

depositing an electroluminescent medium (202, 203, 204) including at least a layer of active emitter material selected for emission of a selected hue and a layer of a low work function metal in the cavity structures (10, 20);

depositing a layer of an ambient stable metal (106, 108, 110) in each of the cavity structures (10, 20) to seal the cavity structures (10, 20)

and form an electrical contact with the low work function metal in the cavity structures (10, 20);

lifting off the layer of photoresist to complete a set of pixels constructed to emit the selected hue; and

positioning metal strips (115) in overlying relationship to the pixels, the metal strips (115) connecting the ambient stable metal (106, 108, 110) in each cavity structure (10, 20) in the pixels into rows, the rows extending in a direction orthogonal to the metal strips (101).

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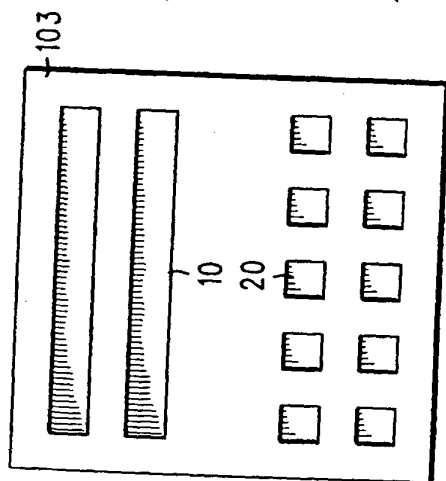


FIG. 1

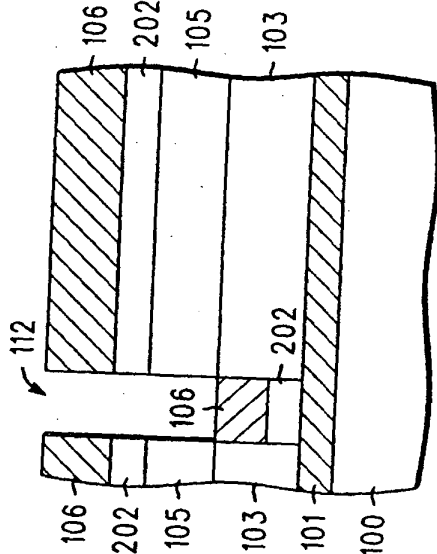


FIG. 2

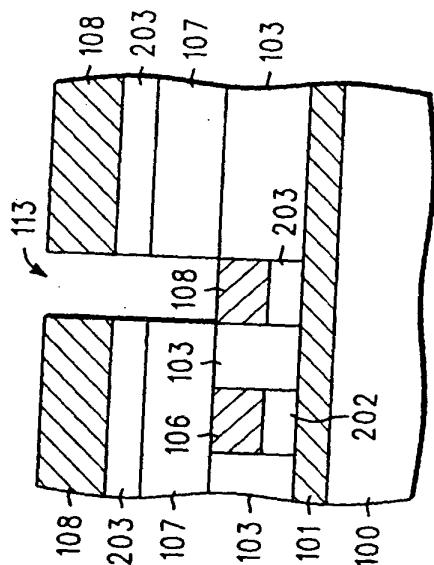


FIG. 3

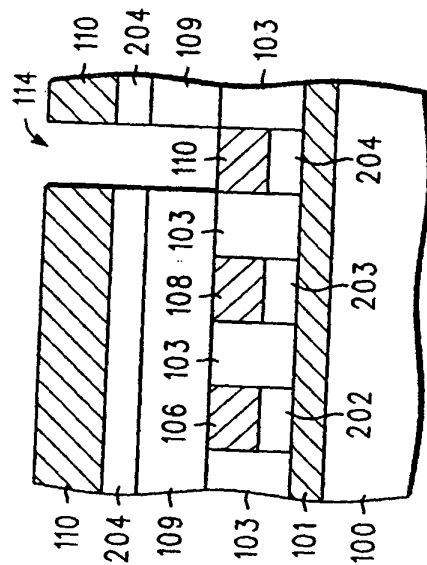


FIG. 4

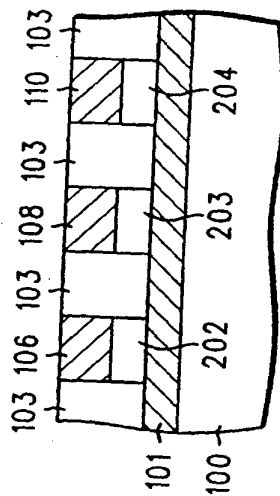


FIG. 5

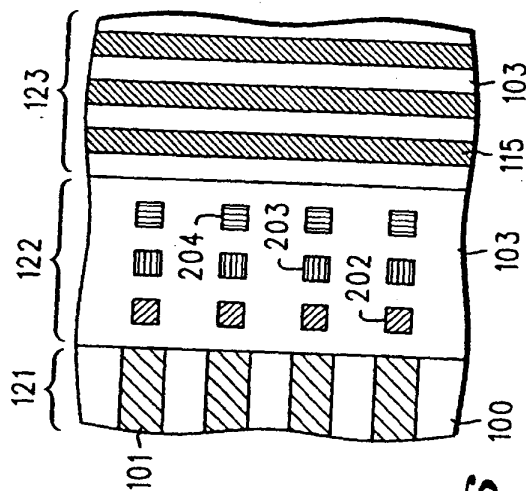
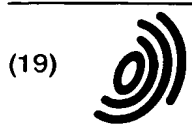


FIG. 6



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(11) **EP 0 734 078 A3**

(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
13.05.1998 Bulletin 1998/20

(51) Int. Cl.⁶: **H01L 51/00**, H01L 27/15,
H01L 33/00

(43) Date of publication A2:
25.09.1996 Bulletin 1996/39

(21) Application number: **96104504.4**

(22) Date of filing: **21.03.1996**

(84) Designated Contracting States:
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(30) Priority: **22.03.1995 US 409129**

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(54) **Full color organic light emitting diode array**

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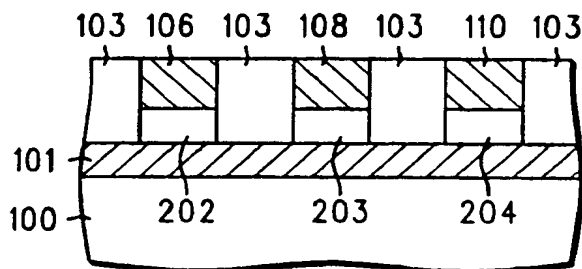


FIG. 5

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EUROPEAN SEARCH REPORT

Application Number
EP 96 10 4504

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	PATENT ABSTRACTS OF JAPAN vol. 016, no. 246 (E-1212), 5 June 1992 & JP 04 051494 A (PIONEER ELECTRON CORP), 19 February 1992, * abstract *	1,2,6,7, 10	H01L51/00 H01L27/15 H01L33/00
A	EP 0 553 496 A (EASTMAN KODAK CO) * figures *	1,10	
D,A	& US 5 276 380 A (TANG) 4 January 1994		
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H01L H05B
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 19 March 1998	Examiner De Laere, A
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